

# Notice of Allowability

Application No.

09/837,882

Examiner

Mai T. Tran

Applicant(s)

PHAM, CHRISTOPHER H.

Art Unit

2129

## -- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 11/02/2005.
2. ☒ The allowed claim(s) is/are 20-26, 28-29 and 33-42.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☐ All b) ☐ Some\* c) ☐ None of the:
    1. ☐ Certified copies of the priority documents have been received.
    2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
    3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  
**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
  - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
    - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
  - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

### Attachment(s)

- |   |   |
|---|---|
| 1. <input type="checkbox"/> Notice of References Cited (PTO-892)  | 5. <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)           |
| 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                                | 6. <input type="checkbox"/> Interview Summary (PTO-413),<br>Paper No./Mail Date _____ |
| 3. <input type="checkbox"/> Information Disclosure Statements (PTO-1449 or PTO/SB/08),<br>Paper No./Mail Date _____ | 7. <input type="checkbox"/> Examiner's Amendment/Comment                              |
| 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit<br>of Biological Material          | 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance  |
|   | 9. <input type="checkbox"/> Other _____   |

### REASONS FOR ALLOWANCE

1. Claims **20-26, 28-29, and 33-42** are allowed.
2. The following is an Examiner's statement of reasons for allowance:  
  
The cited prior art taken alone or in combination fails to teach the claimed invention of linear associative memory-based hardware architecture for fault tolerant ASIC/FPGA work-around, as claimed by Applicant. Specifically, independent claims 20 and 33 disclose using a circuit for checking the integrity of one or more input vectors to a digital hardware block and a programmable logic unit that comprises a plurality of interconnected hardware blocks, a feedforward linear associative memory neural network checking circuit, a software work-around, and the selection circuit connected to the checking circuit.
3. The closest prior art of "Artificial Neural Network Based Multiple Fault Diagnosis in Digital Circuits", IEEE (1998) by A. A. Al-Jumah et al teaches a technique based on the use of Artificial Neural Networks for the diagnosis of multiple faults in digital circuits, but fails to teach or suggest a circuit using a SRAM array and a content-addressable memory for storing the known bad input vectors and also using said SRAM to update both the linear associative memory and the weight matrix and in particular the selection circuit connected to a feedforward linear associative memory neural network checking circuit. To the extent that this feature is not found in the prior art cited by Examiner, the present case is held allowable over the art of record.

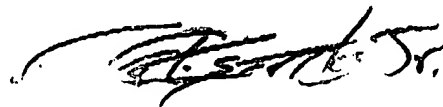
4. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

### CONCLUSION

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Mai T. Tran whose telephone number is (571) 272-4238. The examiner can normally be reached on M-F 9:00am-- 5:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Vincent can be reached on (571) 272-3080. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



M.T.T  
Patent Examiner  
Date: 11/18/2005

Wilbert L. Starks  
Primary Examiner  
Tech Center 2100